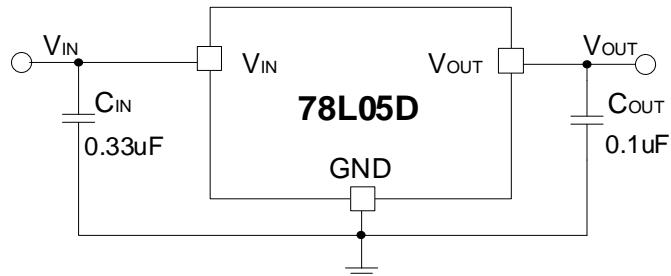


## Features

- Maximum Output Current: 0.1A
- Output Voltage: 5V
- Thermal Overload Protection
- 2% Output Voltage Accuracy

## Typical Application Circuit



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.

## Pin Configuration

SOT89-3	TO92	SOP8																
<p>78L05D YYWW ZZ</p> <p>1 V<sub>OUT</sub>    2 GND    3 V<sub>IN</sub></p>	<p>78L05D YYWW ZZ</p> <p>1 V<sub>OUT</sub>    2 GND    3 V<sub>IN</sub></p>	<p>BL78L05D YYWWZZ</p> <table border="1"> <tr> <td>V<sub>OUT</sub></td> <td>1</td> <td>V<sub>IN</sub></td> <td>8</td> </tr> <tr> <td>GND</td> <td>2</td> <td>GND</td> <td>7</td> </tr> <tr> <td>GND</td> <td>3</td> <td>GND</td> <td>6</td> </tr> <tr> <td>NC</td> <td>4</td> <td>NC</td> <td>5</td> </tr> </table>	V <sub>OUT</sub>	1	V <sub>IN</sub>	8	GND	2	GND	7	GND	3	GND	6	NC	4	NC	5
V <sub>OUT</sub>	1	V <sub>IN</sub>	8															
GND	2	GND	7															
GND	3	GND	6															
NC	4	NC	5															

YY: Stand for the assembly year.  
 WW: Stand for the assembly week.  
 ZZ: Stand for the assembly factory.

## Absolute Maximum Ratings

Parameter		Symbol	Ratings	Unit
Input voltage		V <sub>IN</sub>	42	V
Operating junction temperature range		T <sub>J</sub>	-40 ~ +140	°C
Power dissipation	SOT89-3	P <sub>D</sub>	1.15	W
	TO92		0.76	
	SOP8		0.84	
Thermal resistance (Junction to ambient)	SOT89-3	θ <sub>JA</sub>	100	°C/W
	TO92		151	
	SOP8		136	
Operating ambient temperature range		T <sub>A</sub>	-40 ~ +125	°C
Storage temperature range		T <sub>stg</sub>	-40 ~ +150	°C

**Note:**

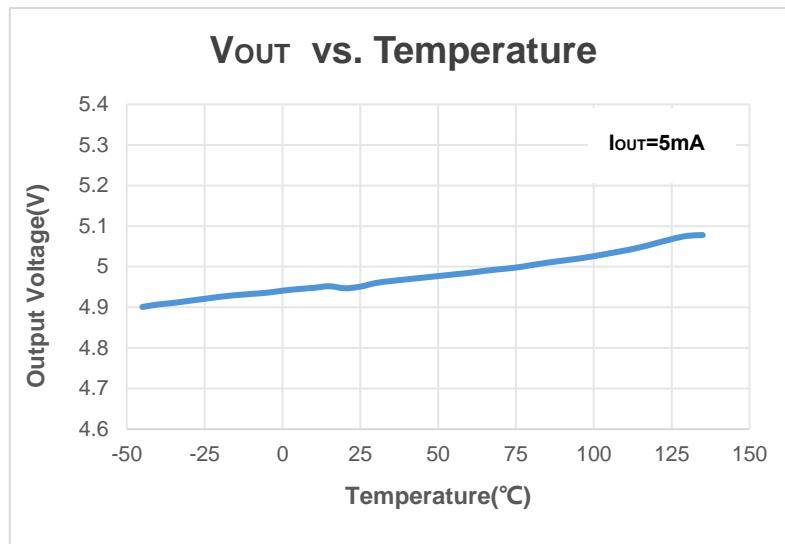
1. Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product.
2. The maximum allowable power dissipation is a function of the maximum junction temperature T<sub>J(MAX)</sub>, the junction-to-ambient thermal resistance θ<sub>JA</sub>, and the ambient temperature T<sub>A</sub>. The maximum allowable continuous power dissipation at any ambient temperature is calculated by P<sub>D(MAX)</sub>=(T<sub>J(MAX)</sub>-T<sub>A</sub>)/θ<sub>JA</sub>.
3. The θ<sub>JA</sub> values given in this table are for comparison with other packages only and cannot be used for design purposes. They do not represent the performance achieved in real-world applications.

## Electrical Characteristics

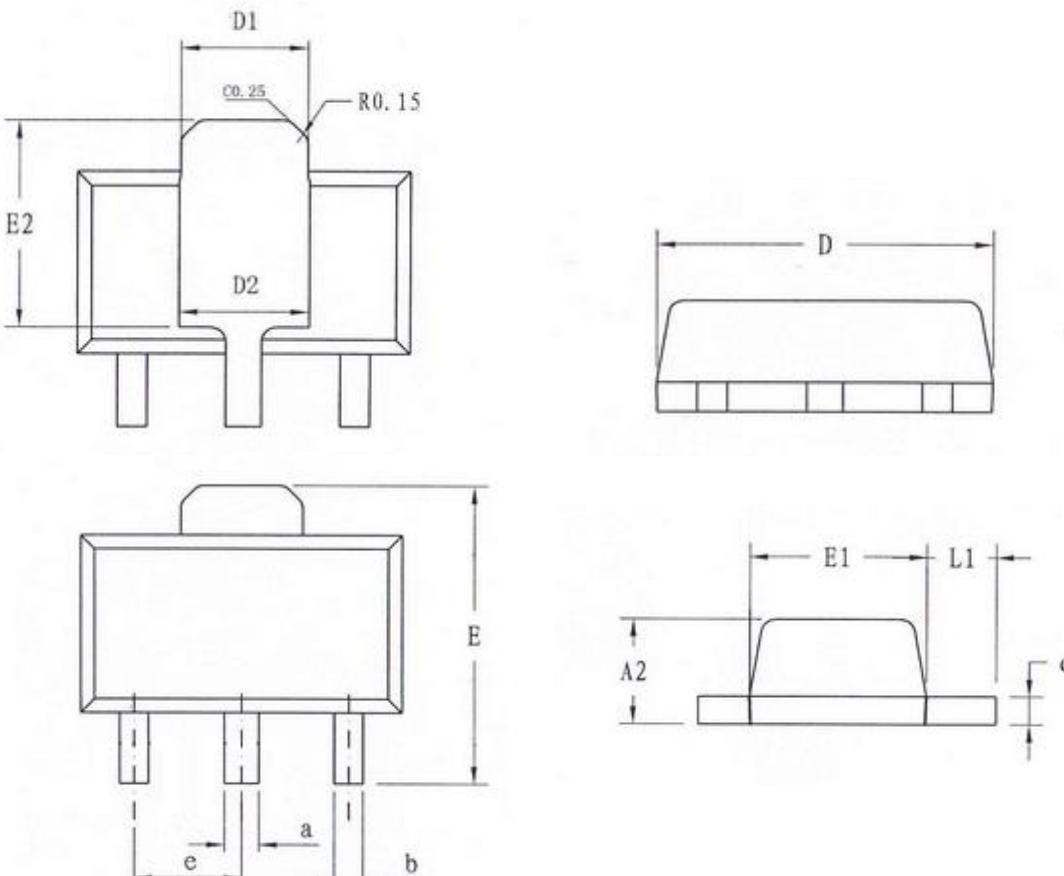
(V<sub>IN</sub>=10V, I<sub>OUT</sub>=40mA, -30°C < T<sub>A</sub> < 85°C, C<sub>IN</sub>=0.33uF, C<sub>OUT</sub>=0.1uF, unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ.	Max	Unit
Output Voltage	V <sub>OUT</sub>	T <sub>A</sub> =25°C	4.9	5	5.1	V
		V <sub>IN</sub> =7V~20V, I <sub>OUT</sub> =1mA~40mA	4.8	-	5.2	
		V <sub>IN</sub> =7V~V <sub>MAX</sub> , I <sub>OUT</sub> =1mA~70mA	4.8	-	5.2	
Load Regulation	ΔV <sub>OUT</sub>	T <sub>A</sub> =25°C, I <sub>OUT</sub> =1mA~100mA	-	11	60	mV
		T <sub>A</sub> =25°C, I <sub>OUT</sub> =1mA~40mA	-	5	6	
Line Regulation	ΔV <sub>OUT</sub>	T <sub>A</sub> =25°C, V <sub>IN</sub> =7V~20V	-	8	150	mV
		T <sub>A</sub> =25°C, V <sub>IN</sub> =8V~20V	-	6	100	
Quiescent Current	I <sub>Q</sub>	-	-	3	5.5	mA
Quiescent Current Change	ΔI <sub>Q</sub>	V <sub>IN</sub> =8V~20V	-	-	1.5	mA
		I <sub>OUT</sub> =1mA~40mA	-	-	0.2	
Output Noise Voltage	V <sub>N</sub>	10Hz≤f≤100kHz	-	63	-	uV
Temperature Coefficient	ΔV <sub>OUT</sub> /ΔT	I <sub>OUT</sub> =5mA		0.65		mV/°C
Ripple Rejection	PSRR	V <sub>IN</sub> =10V~20V, f=120Hz, T <sub>A</sub> =25°C	41	60	-	dB
Dropout Voltage	V <sub>Drop</sub>	T <sub>A</sub> =25°C	-	1.7	-	V

## Typical Performance Characteristics

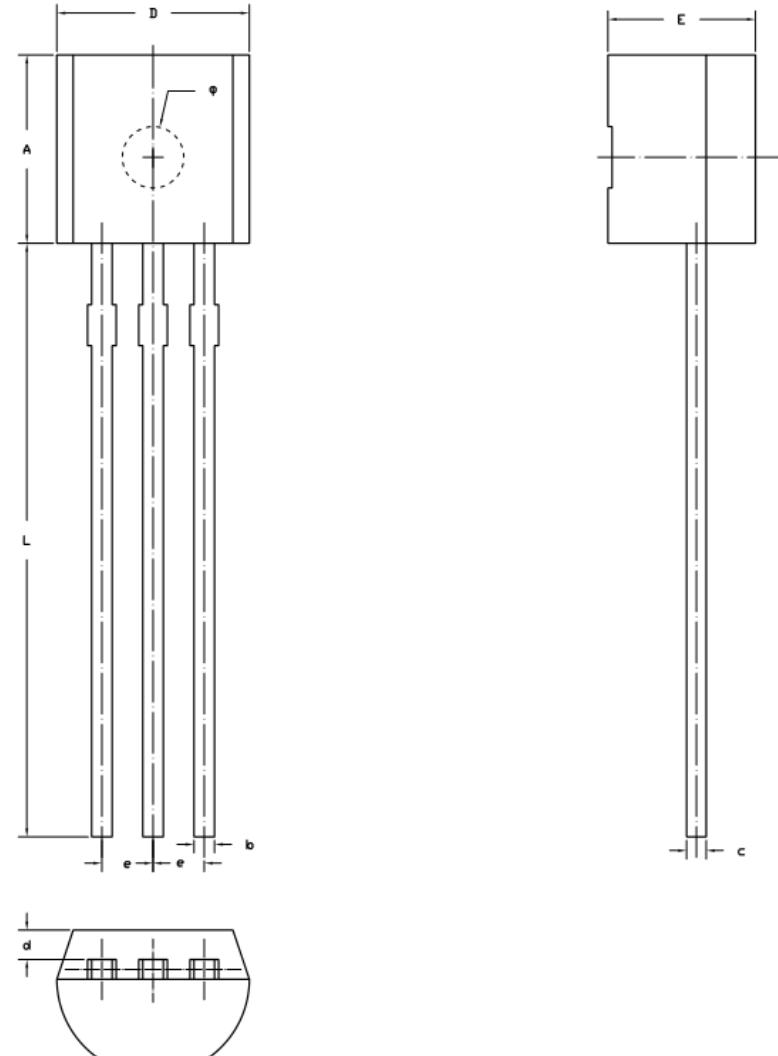


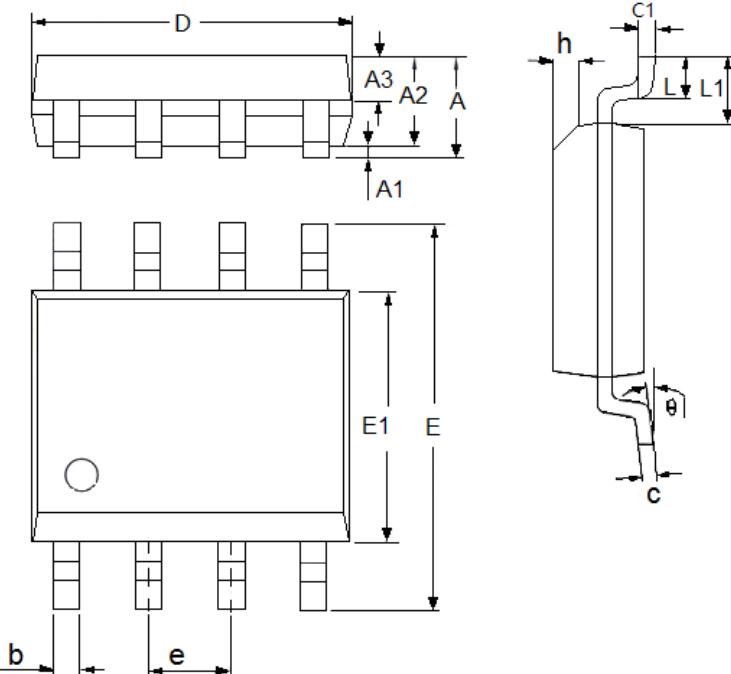
## Package Information

Package	SOT89-3	Devices per reel	1000 pcs
			

DIM	Millimeters		Inches	
	Min	Max	Min	Max
A2	1.4	1.6	0.0551	0.0630
b	0.38	0.46	0.0150	0.0181
a	0.46	0.56	0.0181	0.0220
c	0.38	0.42	0.0150	0.0165
D	4.4	4.6	0.1732	0.1811
D1	1.62	1.83	0.0638	0.0720
E2	2.84(TYP)		0.1118(TYP)	
D2	1.75(TYP)		0.0689(TYP)	
E	3.95	4.25	0.1555	0.1673
E1	2.4	2.6	0.0945	0.1024
e	1.5(TYP)		0.0591(TYP)	
L1	0.89	1.2	0.0350	0.0472

Package	TO92	Devices per bag	1000pcs
	 <p>The diagram illustrates the physical dimensions of the TO92 package. It includes three views: a top view showing lead spacing (A), total width (D), and lead thickness (e); a side view showing height (L) and lead thickness (e); and a cross-sectional view showing lead height (d) and lead thickness (e). Dimension φ indicates the diameter of the circular pad on the underside.</p>		

Package	SOP8	Devices per reel	3000 pcs
	 <p>The drawing shows two views of the SOP8 package. The top view illustrates the footprint with dimensions: width D, height A, and lead spacing A1, A2, A3. The bottom view shows the lead profile with height h, lead length L, lead pitch L1, lead angle θ, and lead thickness c. The side view provides the overall height E and body width E1.</p>		

DIM	Millimeters		Inches	
	Min	Max	Min	Max
A	1.3	1.8	0.0512	0.0709
A1	0.05	0.25	0.002	0.0098
A2	1.25	1.65	0.0492	0.065
A3	0.5	0.7	0.0197	0.0276
b	0.3	0.51	0.0118	0.0201
c	0.17	0.25	0.0067	0.0098
D	4.7	5.1	0.185	0.2008
E	5.8	6.2	0.2283	0.2441
E1	3.8	4	0.1496	0.1575
e	1.27(TYP)		0.05(TYP)	
h	0.25	0.5	0.0098	0.0197
L	0.4	1.27	0.0157	0.05
L1	1.04(TYP)		0.0409(TYP)	
θ	0	8°	0	8°
c1	0.25(TYP)		0.0098(TYP)	